

JHK75N120FA

Product Preview

1200V/75A FIELD-STOP TRENCH IGBT WITH DIODE

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Features

- Low V_{CE(sat)}
- Fast Switching
- High Ruggedness
- Short-Circuit Rated



Product Summary				
V _{CES} 1200V				
lc	75A ⁽¹⁾			
V _{CE(sat),typ}	1.65V (T」 = 25°C)			
Package	TO-264			

Applications

- Inverters
- Frequency Converters
- Industrial Motor Drives
- Uninterrupted Power Supply

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Ordering Information

Part Number	Marking	Package	Packing
JHK75N120FA	HK75N120FA	TO-264	Tube

Absolute Maximum Ratings

Parameter	Symbol	Limit	Unit	
Collector-to-Emitter Voltage	V _{CES}	1200	- v	
Gate-to-Emitter Voltage	V _{GES}	±20	v	
DC Collector Current (T _c = 25°C, T _J = 150°C)		120 (2)		
DC Collector Current (T _c = 100°C, T _J = 150°C)	- I _C	78		
Pulsed Collector Current (pulse width limited by maximum T_J)	I _{CM}	225		
Diode Forward Current ($T_c = 25^{\circ}C$, $T_J = 150^{\circ}C$)		120 (2)	A	
Diode Forward Current ($T_c = 100^{\circ}C$, $T_J = 150^{\circ}C$)	- I _F	79		
Diode Pulsed Current (pulse width limited by maximum T_J)	I _{FM}	225		
Short Circuit Withstand Time (V_{GE} = 15V, $V_{CC} \le 600V$, $T_{J_{start}} \le 150^{\circ}C$)	t _{sc}	10	μs	
Turn-off Safe Operating Area ($V_{CE} \le 1200V$, $T_J \le 150^{\circ}C$)	-	225	А	
Maximum Power Dissipation ($T_c = 25^{\circ}C$, $T_J = 150^{\circ}C$)	P _{D(max)}	521	W	
Operating Junction Temperature	TJ	-40 to +150		
Storage Temperature	T _{stg}	-55 to +150	°C	
Maximum Lead Temperature for Soldering (1/8" from case for 5 seconds)	T _{sld}	260		

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Static Electrical Characteristics ⁽³⁾

Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit
Collector-to-Emitter Breakdown Voltage	BV _{CES}	V_{GE} = 0V, I _C = 250µA	1200	-	-	V
		V_{CE} = 1200V, V_{GE} = 0V	-	-	10	μΑ
Collector-to-Emitter Leakage Current	I _{CES}	V _{CE} = 1200V, V _{GE} = 0V,			F	
		T _J =150°C	-	-	5	mA
Gate-to-Emitter Leakage Current	I _{GES}	$V_{CE} = 0V, V_{GE} = \pm 20V$	-	-	100	nA
Gate Threshold Voltage	V _{GE(th)}	$V_{CE} = V_{GE}$, $I_C = 1.5 mA$	5.5	6.5	7.5	
		V _{GE} = 15V, I _C = 75A	-	1.65	2.0	
Collector-to-Emitter Saturation Voltage	$V_{CE(sat)}$	V _{GE} = 15V, I _C = 75A,	_	2.2	-	
		T _J =150°C		2.2		V
		V_{GE} = 0V, I_{F} = 75A	-	1.8	2.25	
Diode Forward Voltage	VF	V _{GE} = 0V, I _F = 75A	_	1.6	-	
		T _J =150°C		1.0		

Thermal Characteristics

Parameter	Symbol	Min	Тур	Max	Unit
Junction-to-Ambient Thermal Resistance	R _{θJA}	-	-	25	
Junction-to-Case Thermal Resistance, IGBT		-	-	0.24	°C/W
Junction-to-Case Thermal Resistance, Diode	R _{θJC}	-	-	0.33	

Dynamic Electrical Characteristics ⁽³⁾

Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit
Total Gate Charge	Qg	V _{CC} = 600V, V _{GE} = 15V, I _C = 75A	-	328	-	nC
Input Capacitance	C _{iss}	V _{CF} = 25V,	-	7334	-	
Output Capacitance	C _{oss}	$V_{GE} = 0V,$	-	292	-	pF
Reverse Transfer Capacitance	C _{rss}	f = 1MHz	-	97	-	

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Switching Characteristics, Inductive Load ^{(3), (4)}

Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit
Turn-on Delay time	t _{d(ON)}	N 600V	-	60	-	
Rise Time	tr	V _{CC} = 600V, V _{GE} = 0/15V,	-	84	-	
Turn-off Delay time	t _{d(OFF)}	R _G = 1Ω, I _C = 75A,	-	230	-	ns
Fall Time	t _f	$L_{load} = 0.82 mH$,	-	123	-	
Turn-On Switching Loss	Eon	Energy losses include "tail" and diode	-	6.94	-	
Turn-Off Switching Loss	E _{off}	reverse recovery.	-	3.58	-	mJ
IGBT Total Switching Loss	E _{ts}		-	10.52	-	
Diode Reverse-Recovery Time	t _{rr}	V _B = 600V,	-	287	-	ns
Diode Reverse-Recovery Charge	Q _{rr}	I _F = 75A,	-	3980	-	nC
Diode Peak Reverse-Recovery Current	I _{rrm}	dl _F /dt = 720A/µs	-	27	-	А
Short Circuit Collector Current	I _{C(SC)}	$V_{GE} = 15V,$ $V_{CC} \le 600V,$ $t_{SC} \le 10 \mu s$	-	310	_	A

(1) DC collector current, $T_c = 102^{\circ}C$, $T_J = 150^{\circ}C$.

(2) Limited by bonding wire

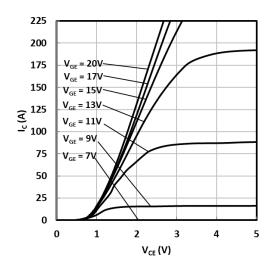
(3) $T_J = 25^{\circ}C$ unless otherwise specified

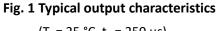
(4) t_r : from 10% of Ic to 90% of Ic; t_f : from 90% of Ic to 10% of Ic;

 E_{on} : from 10% of V_{GE} to 10% of $V_{CE};~~E_{off}$: from 90% of V_{GE} to 10% of Ic.

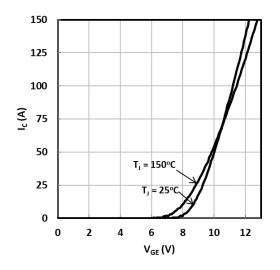


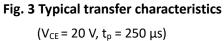
Typical Electrical Characteristics





(T_J = 25 °C, t_p = 250 μs)





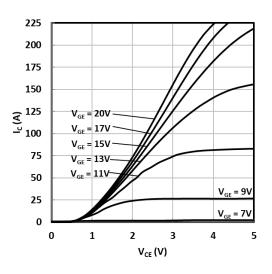


Fig. 2 Typical output characteristics

(T_J = 150 °C, t_p = 250 μs)

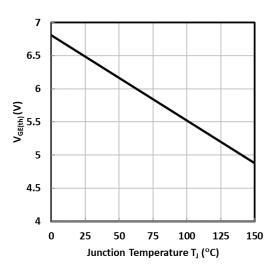


Fig. 4 Typical gate threshold voltage as a function of junction temperature

 $(V_{CE} = V_{GE}, I_{C} = 1.5 \text{mA})$

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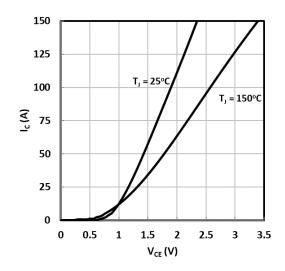


Fig. 5 Typical saturation voltage characteristics

 $(V_{GE} = 15 \text{ V, } t_p = 250 \text{ } \mu\text{s})$

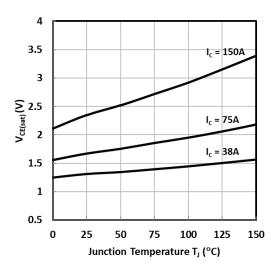
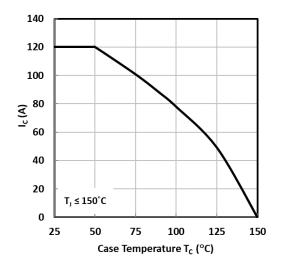
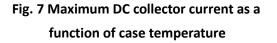


Fig. 6 Typical saturation voltage as a function of junction temperature

 $(V_{GE} = 15 V, t_p = 250 \mu s)$





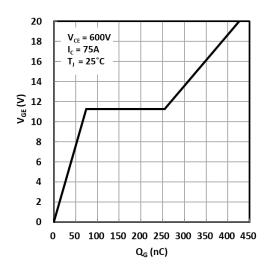


Fig. 8 Typical gate charge characteristics

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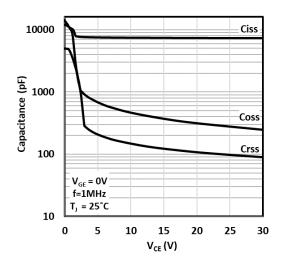


Fig. 9 Typical capacitance as a function of collector-to-emitter voltage

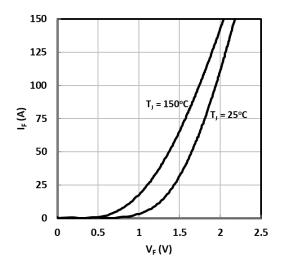
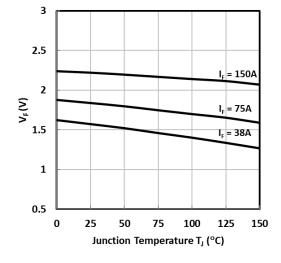
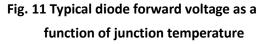


Fig. 10 Typical diode forward current as a function of forward voltage $(V_{GE} = 0 \ V, \ t_p = 250 \ \mu s)$

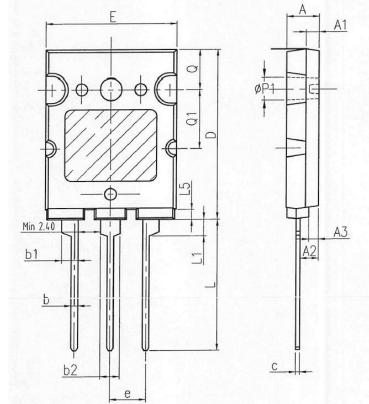


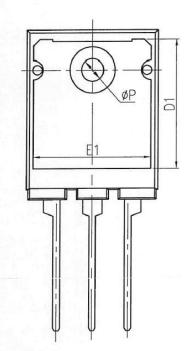


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Package Drawing





CUMPOI		mm	
SYMBOL	MIN	NOM	MAX
А	4.80	5.00	5.20
A1	2	2.00	REF
A2	2.50	2.80	3.10
A3		1.50	REF
b	0.90	1.00	1.25
b1	2.30	2.50	2.75
b2	2.80	3.00	3.20
С	0.50	0.60	0.85
D	25.70	26.00	26.30
D1	19.00	-	-
Е	19.50	20.00	20.50
E1	16.00	The second second second	-
е		5.45	ТҮР
L	19.50	20.00	20.50
L1	2.20	2.50	2.70
L5		1.35	REF
ΦP	3.00	3.20	3.40
ΦΡ1	3.20	3.40	3.60
Q	5.80	6.00	6.20
Q1	8.80	9.00	9.20

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Revision history of JHK75N120FA Specification

Version	Change Items	Effective Date
1.00	Initial Release	May-2021
1.01	DC spec. updates	Aug-2021

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